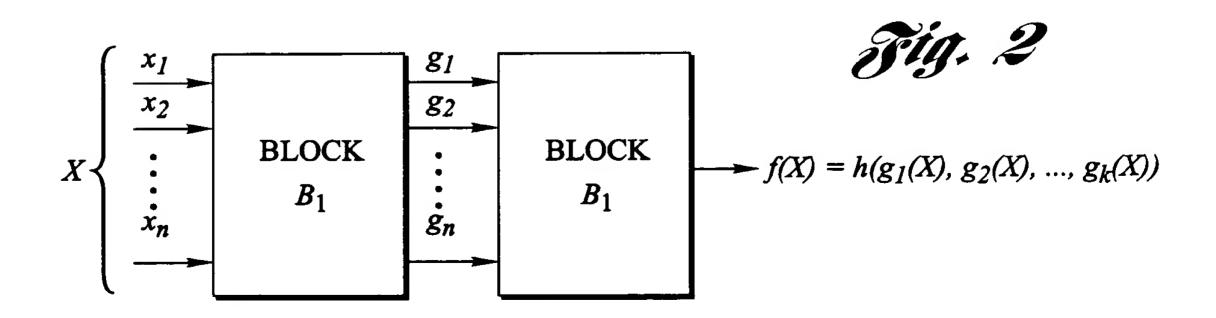
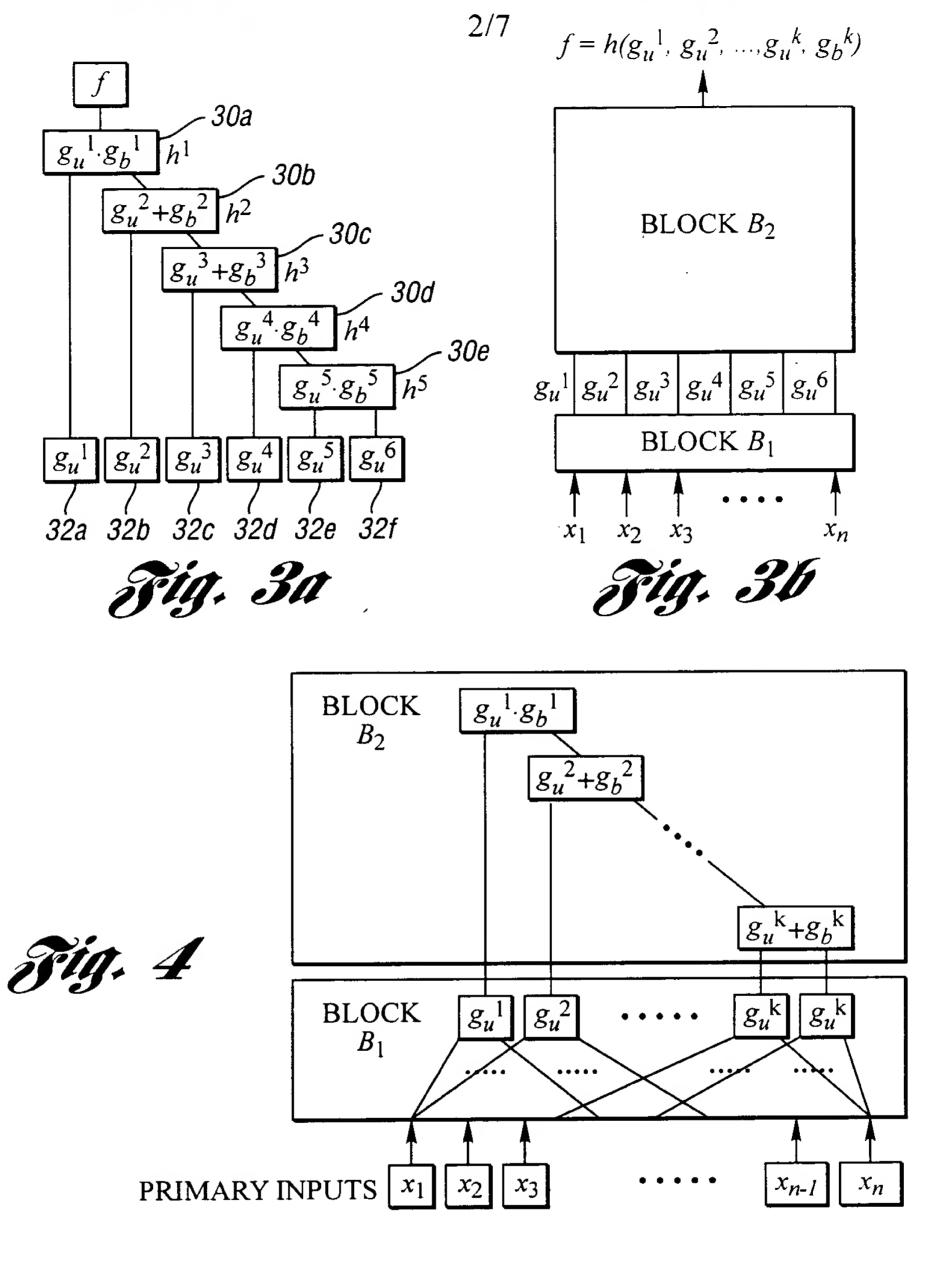
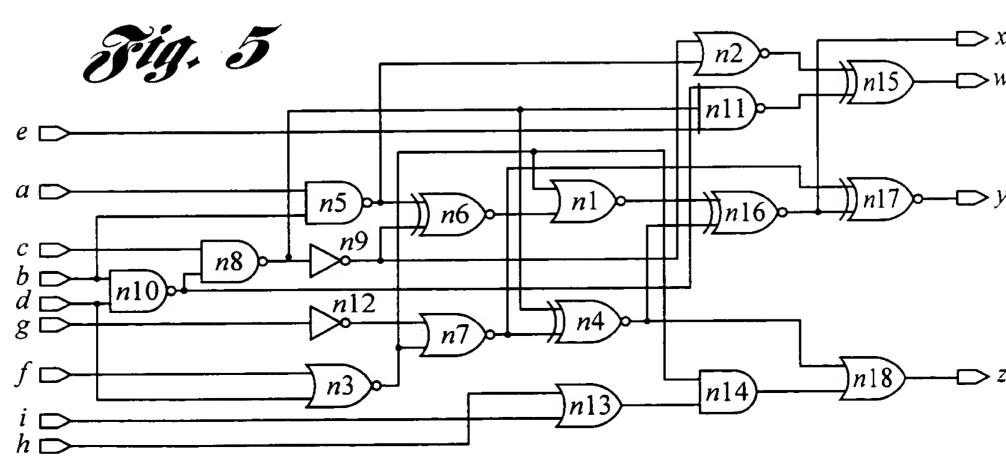
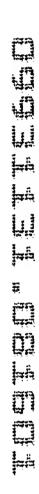


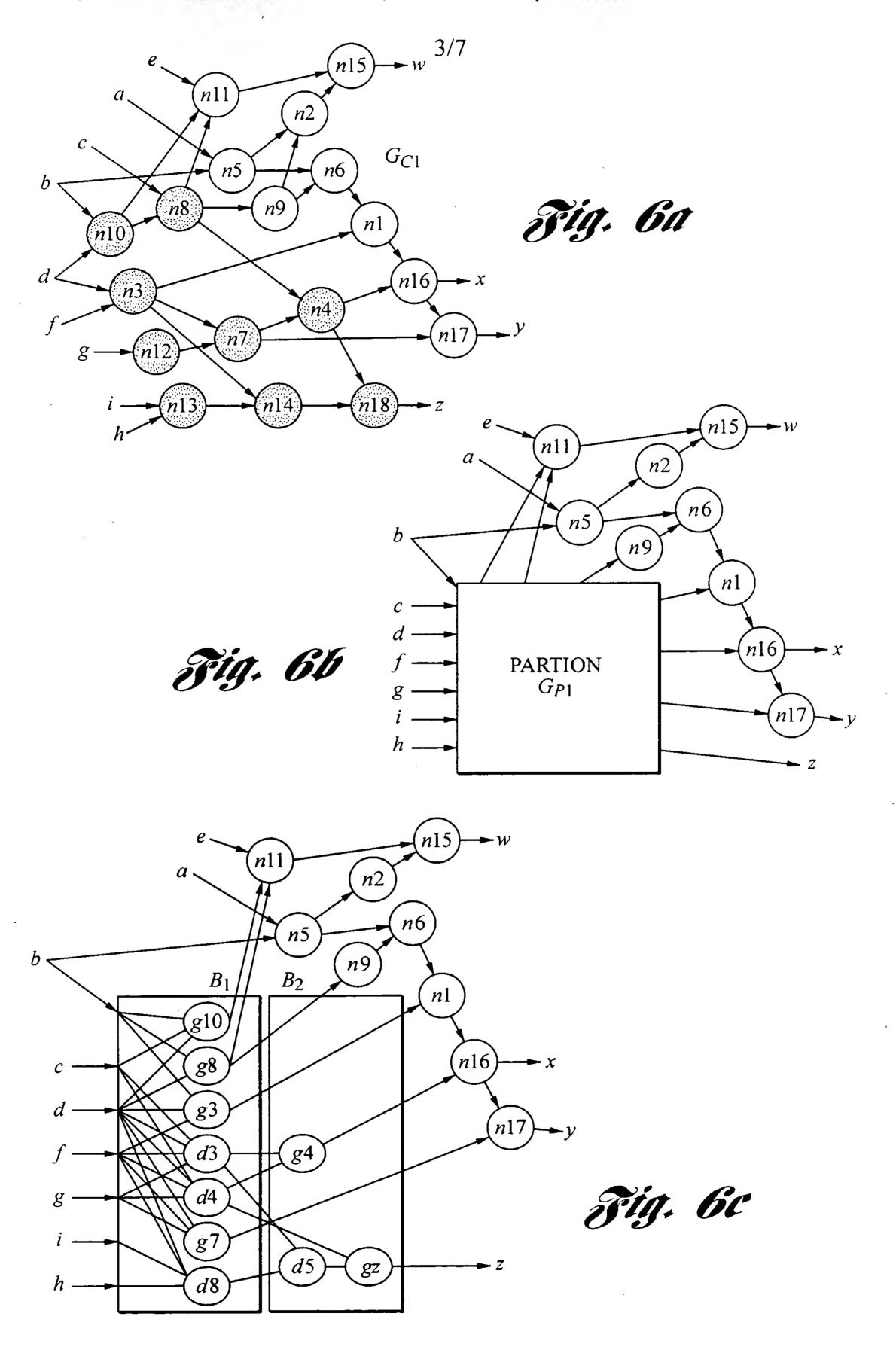
Fig. 1c

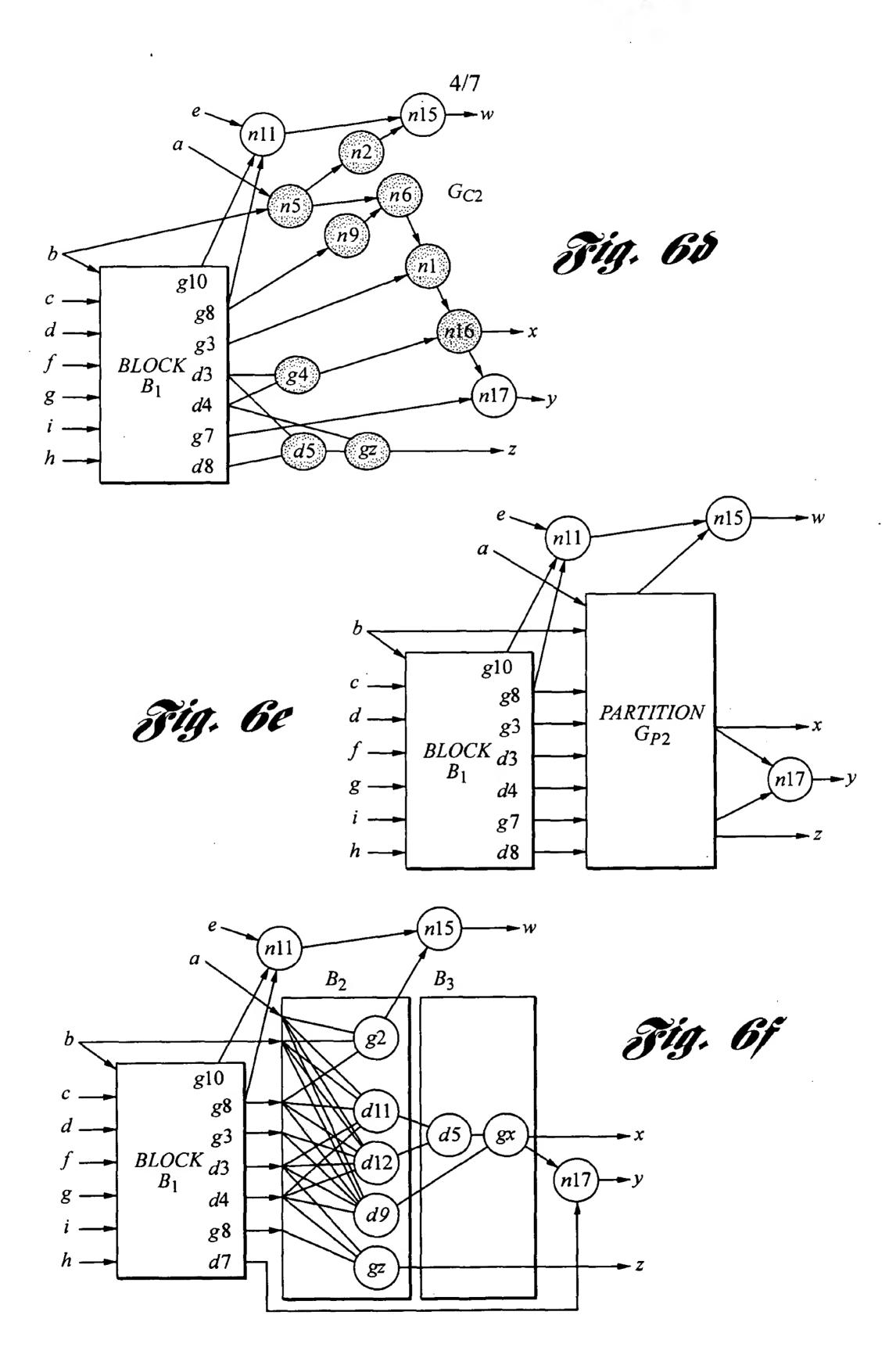






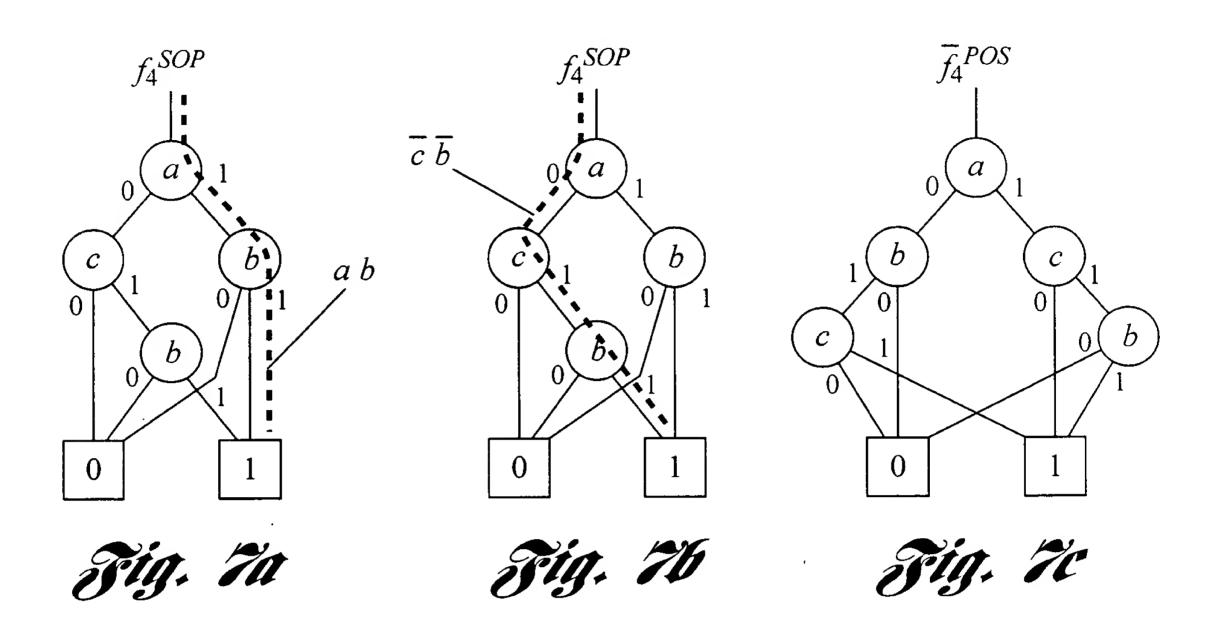






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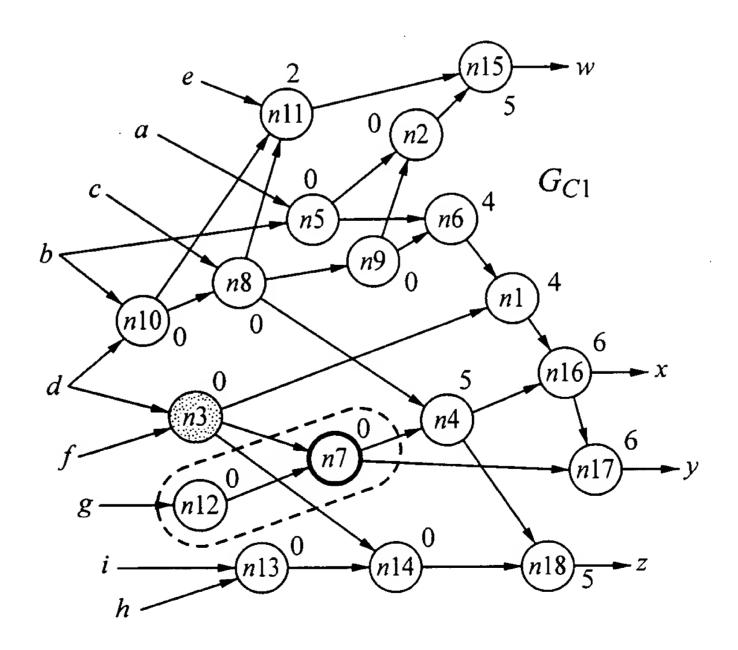


Fig. Sa

Title: METHOD AND SYSTEM FOR SYNTHESIZING A CIRCUIT SPRESENTATION INTO A NEW CUIT REPRESENTATION HAVING GREAT UNATENESS
First Named Inventor: YUNGWON KIM, Ph.D. et al. / Atty. Docket No.: UOM 0209 PUSP

